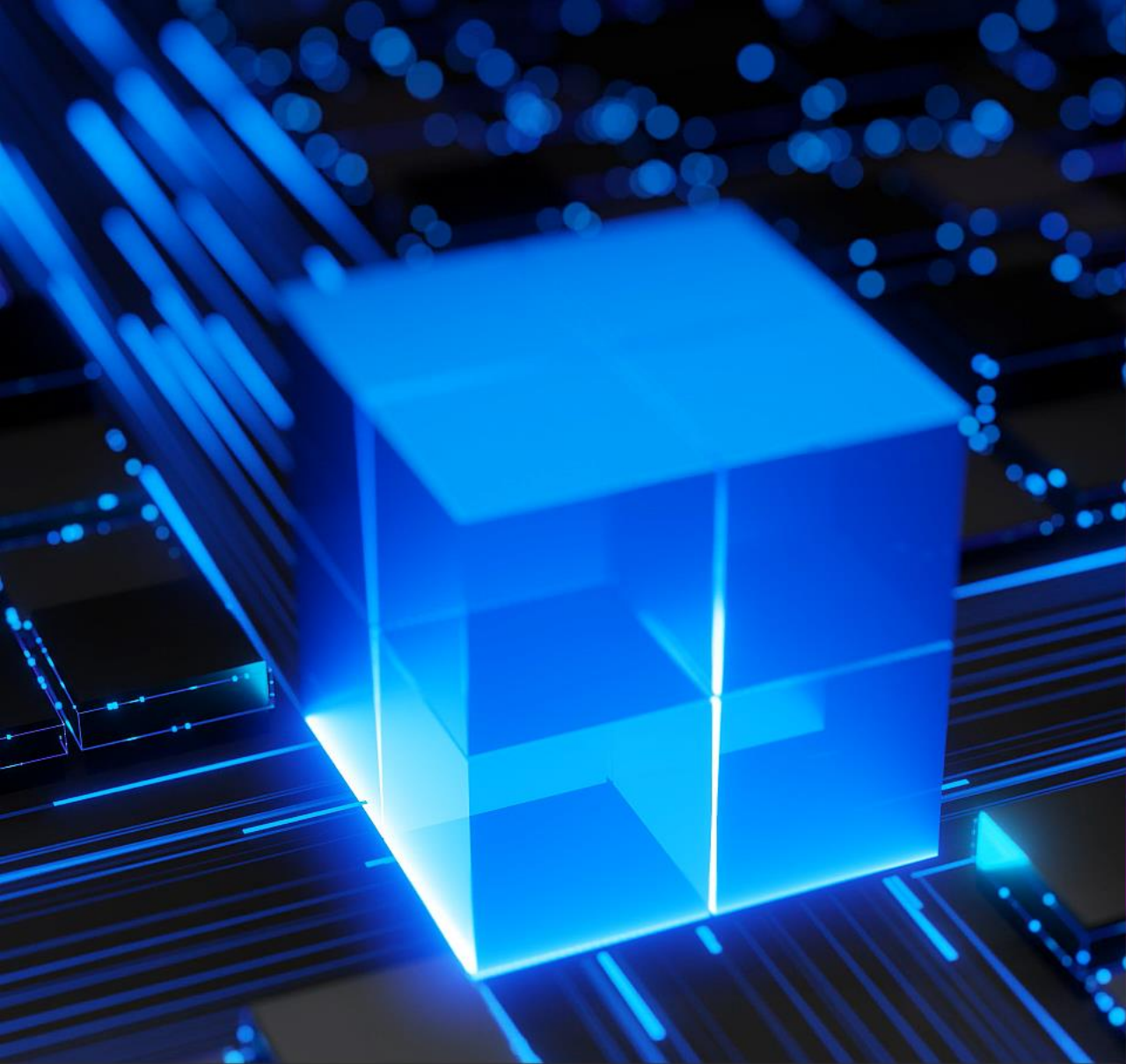


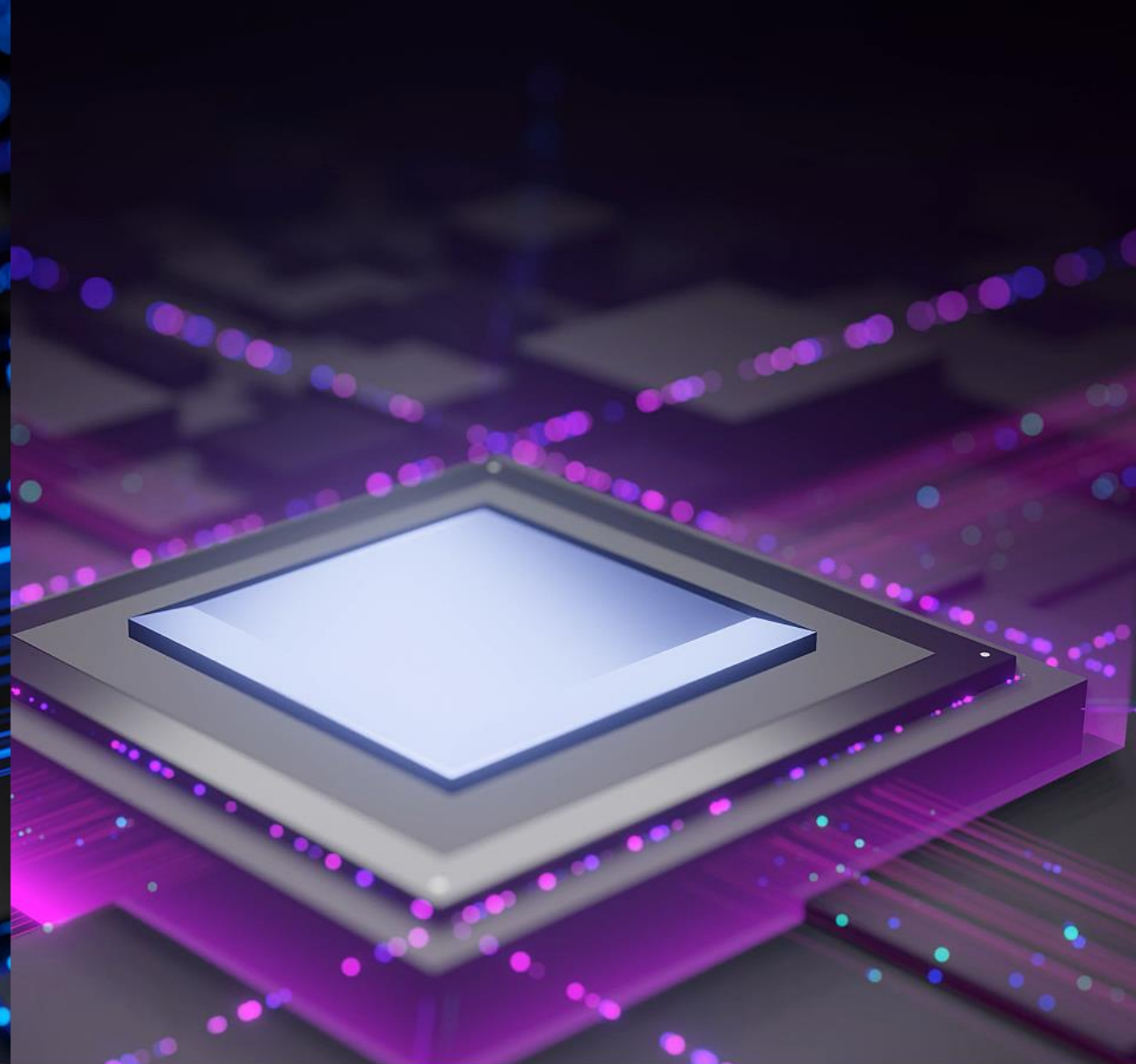


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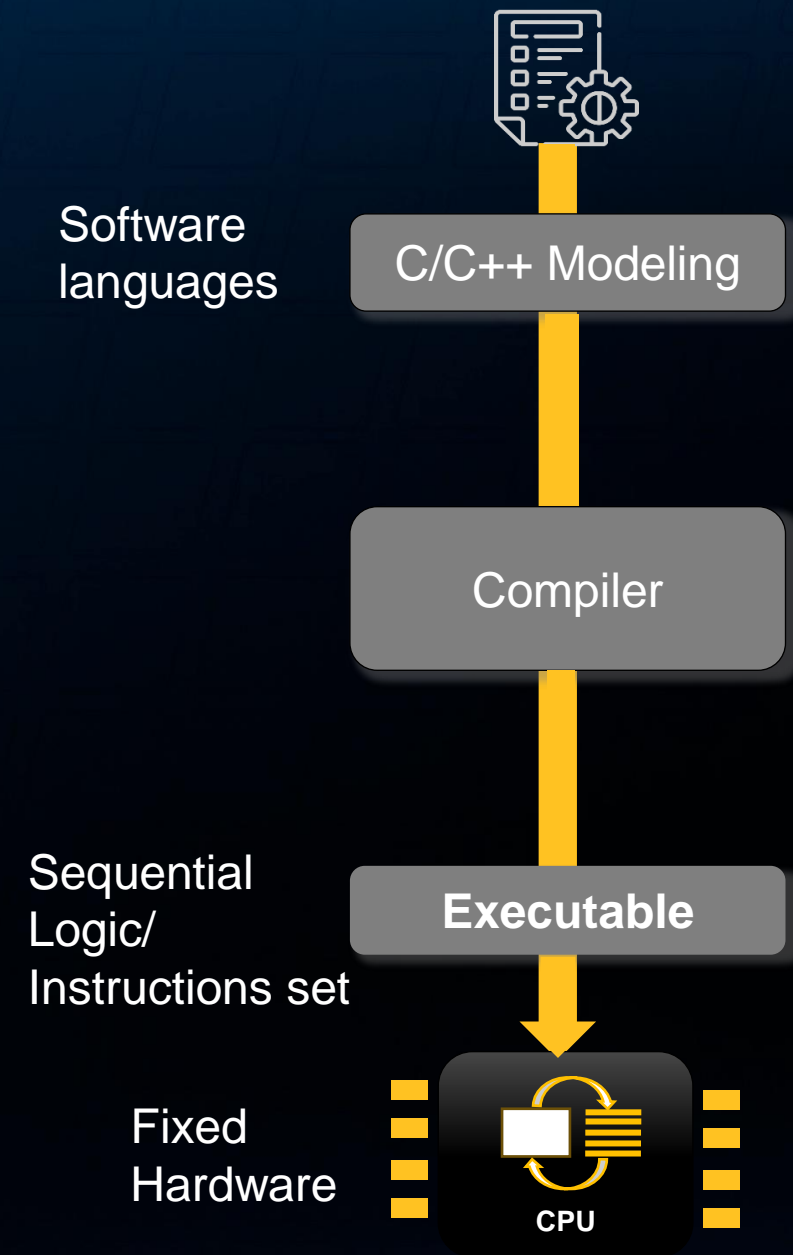
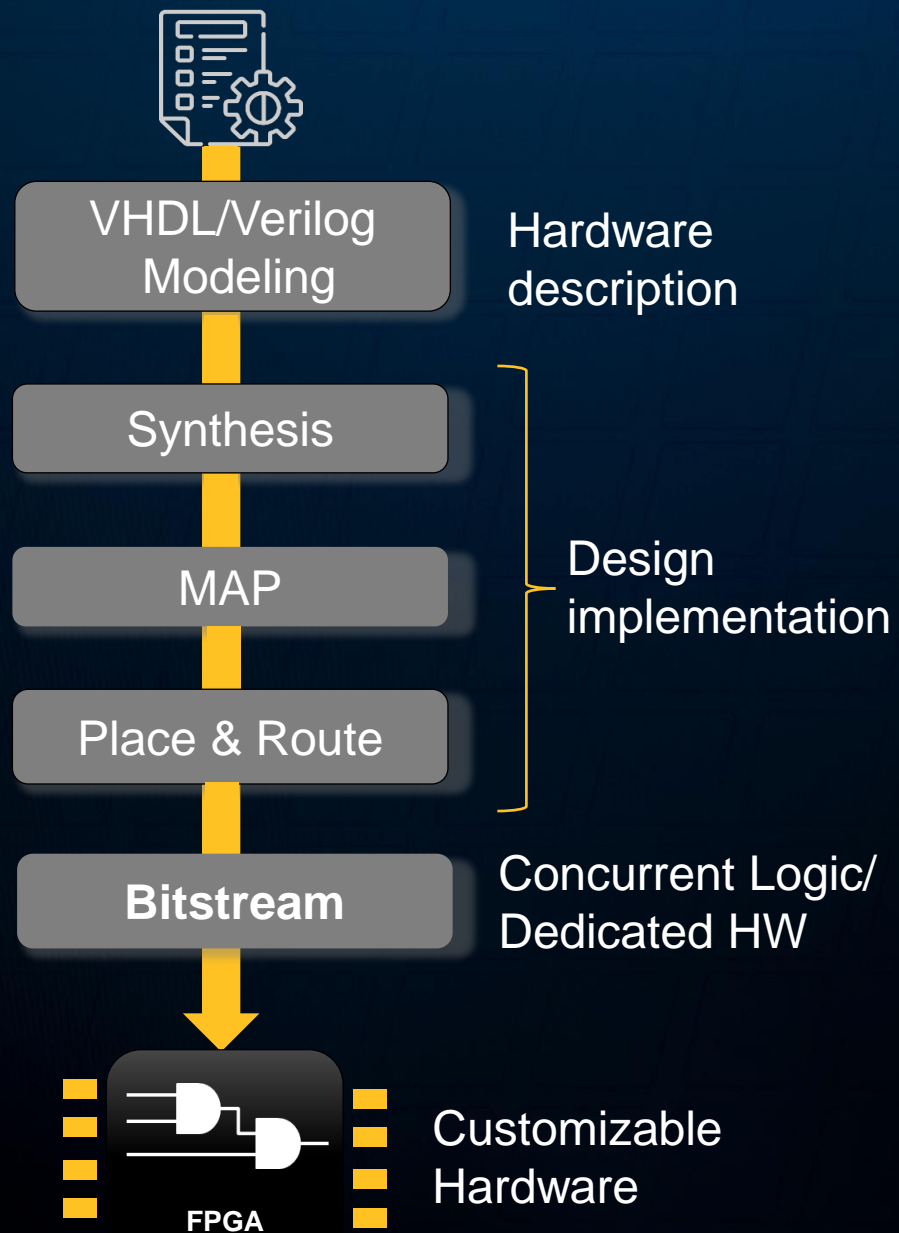
Programming Flow: FPGA vs Microcontroller

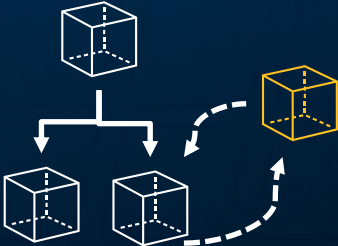





**FPGA - Programmable,
Parallel Processing**

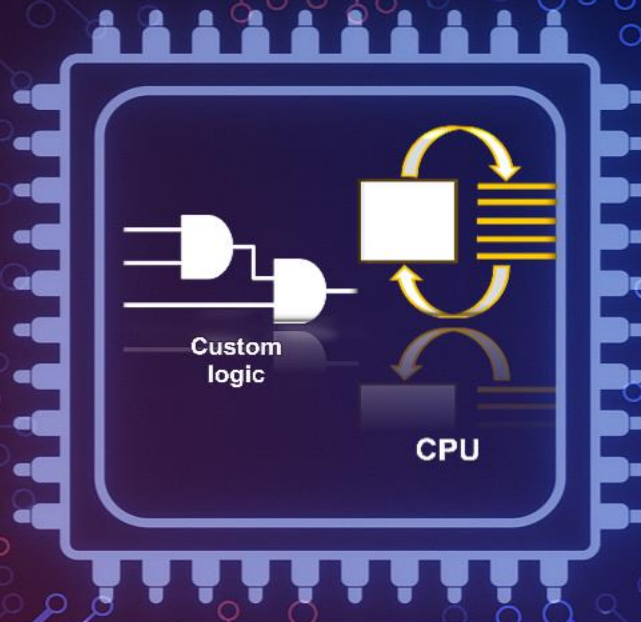


**Microcontroller - Fixed
architecture, sequential processing**



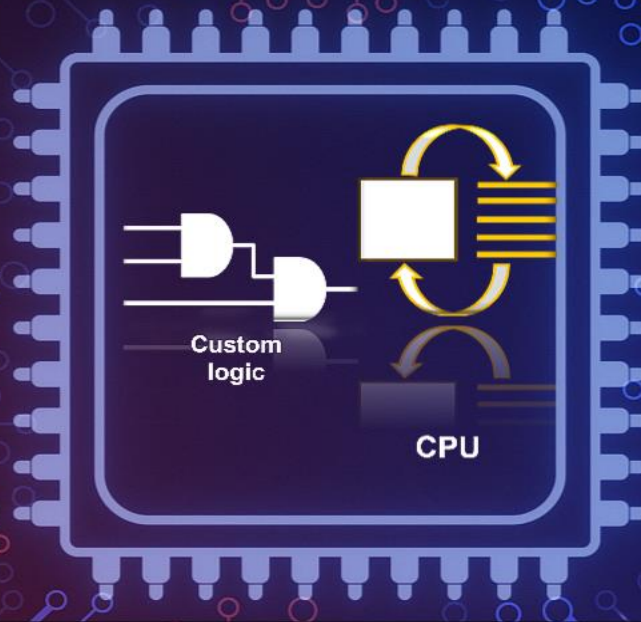
Features	FPGA Programming	Micro Controller Programming
Architecture & Execution	 Parallel & customizable	 Sequential & fixed instruction set
Reconfigurability	Fully reconfigurable	Fixed hardware, programmable software
Programming Model	Hardware Description Language (HDL)	Software languages (C/C++, Python)
Abstraction level	Low level (hardware design)	High level (algorithmic design)
Performance	 Deterministic latency	 General-purpose versatility
Power consumption	Low power for specific tasks	Generally high-power consumption

FPGA System-on-Chip (SoC)



Combination of reconfigurable logic and
soft processor

FPGA System-on-Chip (SoC)



Flexible, low risk & low cost embedded system

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